Binary Reverse Engineering And Analysis Course 7: Mitigations and Bypasses

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- Last time we studied ROP and ASLR
- Some information regarding the GOT
- Stack buffer overflows are pretty dangerous.
- What mitigations are available?

Today

- Preventing return address overflows (SSP)
- How dynamic linking works at runtime
- More advanced mitigations

- Linux (gcc) and Windows (cl) adopt similar strategies
- Buffers are moved to the bottom of the stack frame
- A magic value is placed after all allocated variables and buffers
- Before returning, the magic value is checked
- Called: cookie or canary or guard

Stack smashing protector (Linux)

Internet and the second second second public main main proc near i= byte ptr -7Ch j= byte ptr -78h k = byte ptr -74hbuf= byte ptr -70h stack guard= gword ptr -8 ; __unwind { push rbp mov rbp, rsp add rsp. OFFFFFFFFFFFFFFF80h mov rax, fs:28h copy magic value mov [rbp+stack_guard], rax ; write to stack xor eax. eax 100 rsi, [rbp+buf] lea rcx. [rbp+k] 100 rdx. [rbp+1] lea rax, [rbp+i] mov r8, rsi mov rsi. rax 100 rdi aDDDS : "%d %d %d %s\n" mov eax, 0 ____isoc99_scanf call mov eax. 0 mov rdi, [rbp+stack_guard] ; read from stack rdi, fs:28h xor : check against magic value jz short locret 4011AD 🚺 📬 🔅 🚺 🛃 🔛 stack_chk_fail call locret_4011AD: leave retn 1 // starts at 401156

On Linux: compile with '-fstack-protector' (off by default)

Stack smashing protector (Windows)

sub_140001BB0 proc near

var1= gword ptr -38h var2= gword ptr -28h stack guard= gword ptr -20h arg 8= byte ptr 10h push rsi push rdi push rbx aub rsp, 40h mov rai, rex lea rbx, [rsp+58h+arg_8] mov [rbs+10h], r9 [rbx+8], r8 mov mov [rbs], rds mov rax, cs: security cookie ; copy magic value xor rax, rsp : xor with current stack pointer [rsp+58h+stack_guard], rax ; write to stack mov [rsp+58h+var2], rbs mov mov ecx. 1 call _acrt_iob_func mov rdi. rax call sub 140001CB0 mov res. [ras] mov [rsp+58h+var1], rbx xor r9d, r9d mov rdx, rdi mov r8. rai call sub 140459B10 mov esi, eax mov rcx, [rsp+58h+stack_guard] ; read from stack xor rex. rsp : xor with current stack pointer call security check cookie ; check in dedicated function mon eax, esi add rap. 40h pop rbx pop rdi pop rsi retn sub 140001BB0 endp

■ On Windows: compile with '/GS' (on by default)

SSP pros and cons

• On Linux, the original value is at a hard-to-determine address

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- On Windows, the original value is in the .data section
- However, it is xored with rsp for added security

SSP pros and cons

- On Linux, the original value is at a hard-to-determine address
- On Windows, the original value is in the .data section
- However, it is xored with rsp for added security
- In both cases, there are scenarios where it does not protect from overflows. Which?

The end of buffer overflows?

■ Maybe... but not really.

The end of buffer overflows?

- Maybe... but not really.
- Information leaks (very common)
- Buffer underflows can also occur
- Out-of-bounds access (very common)
 - Relative read/write (jump over the cookie)
 - Absolute read/write
- Heap abuse (dynamic allocation)

RELRO mitigation intro

- Protects the GOT table
- To understand why, let's dig into dynamic linking
- Through this mitigation we'll learn a new exploitation avenue

Dynamic linking (1/3)



A program function calls puts("Hello, world")

Dynamic linking (2/3)



Puts() is actually a stub that uses a pointer from another table

Dynamic linking (2/3)



Dynamic linking (3/3)



Global Offset Table entries (filled in at runtime)

Symbol resolution algorithm $1/3\,$

- The GOT is initially almost empty (lazy loading)
- Only the entry at index 0 is filled in
- Index 0: generic resolver function in Id-linux

Symbol resolution algorithm 2/3

■ All other entries are stubs that call the resolver

gdb-p	eda\$ teles	scop	e 0x404000	30						
0000	0×404000	>	0x403e20	> 0>	<1					
0008	0×404008	>	0x7ffff71	ffe190	> 0×0)				
0016	0×404010	>	0x7ffff71	fea440	(<_dl_r	untime_	resolve	e_xsave>:	push	rbx)
0024	0×404018	>	0×401036	(<fre€< th=""><th>e@plt+6></th><th></th><th>push</th><th>0×0)</th><th></th><th></th></fre€<>	e@plt+6>		push	0×0)		
0032	0x404020	>	0×401046	(<unli< th=""><th>ink@plt⊣</th><th>6>:</th><th>push</th><th>0×1)</th><th></th><th></th></unli<>	ink@plt⊣	6>:	push	0×1)		
0040	0x404028	>	0×401056	(<_exi	it@plt+€	i>:	push	0×2)		
0048	0x404030	>	0x401066	(<frea< th=""><th>ad@plt+€</th><th>i>:</th><th>push</th><th>0×3)</th><th></th><th></th></frea<>	ad@plt+€	i>:	push	0×3)		
0056	0×404038	>	0×401076	(<fclo< th=""><th>se@plt+</th><th>6>:</th><th>push</th><th>0×4)</th><th></th><th></th></fclo<>	se@plt+	6>:	push	0×4)		
0064	0×404040	>	0×401086	(<oper< th=""><th>ndir@plt</th><th>+6>:</th><th>push</th><th>0×5)</th><th></th><th></th></oper<>	ndir@plt	+6>:	push	0×5)		
0072	0×404048	>	0×401096	(<strl< th=""><th>len@plt+</th><th>6>:</th><th>push</th><th>0×6)</th><th></th><th></th></strl<>	len@plt+	6>:	push	0×6)		
0080	0×404050	>	0x4010a6	(<clos< th=""><th>sedir@pl</th><th>t+6>:</th><th>push</th><th>0×7)</th><th></th><th></th></clos<>	sedir@pl	t+6>:	push	0×7)		
0088	0x404058	>	0×4010b6	(<srar< th=""><th>nd@plt+€</th><th>i>:</th><th>push</th><th>0×8)</th><th></th><th></th></srar<>	nd@plt+€	i>:	push	0×8)		
0096	0x404060	>	0x4010c6	(<stro< th=""><th>:mp@plt+</th><th>6>:</th><th>push</th><th>0×9)</th><th></th><th></th></stro<>	:mp@plt+	6>:	push	0×9)		
0104	0×404068	>	0x4010d6	(≺time	e@plt+6>	•:	push	0xa)		
0112	0x404070	>	0x4010e6	(<x< th=""><th>stat@plt</th><th>+6>:</th><th>push</th><th>0×b)</th><th></th><th></th></x<>	stat@plt	+6>:	push	0×b)		
0120	0x404078	>	0x4010f6	(<read< th=""><th>dir@plt</th><th>+6>:</th><th>push</th><th>0xc)</th><th></th><th></th></read<>	dir@plt	+6>:	push	0xc)		
0128	0×404080	>	0×401106	(<fsee< th=""><th>ek@plt+6</th><th>i>:</th><th>push</th><th>0×d)</th><th></th><th></th></fsee<>	ek@plt+6	i>:	push	0×d)		
0136	0×404088	>	0×401116	(<ptra< th=""><th>ace@plt+</th><th>6>:</th><th>push</th><th>0xe)</th><th></th><th></th></ptra<>	ace@plt+	6>:	push	0xe)		
0144	0x404090	>	0×401126	(<aspi< th=""><th>rintf@pl</th><th>t+6>:</th><th>push</th><th>0xf)</th><th></th><th></th></aspi<>	rintf@pl	t+6>:	push	0xf)		
0152	0×404098	>	0×401136	(<mpro< th=""><th>otect@pl</th><th>t+6>:</th><th>push</th><th>0×10)</th><th></th><th></th></mpro<>	otect@pl	t+6>:	push	0×10)		
0160	0x4040a0	>	0×401146	(<fope< th=""><th>en@plt+6</th><th>i>:</th><th>push</th><th>0×11)</th><th></th><th></th></fope<>	en@plt+6	i>:	push	0×11)		
0168	0x4040a8	>	0×401156	(<rena< th=""><th>ame@plt+</th><th>6>:</th><th>push</th><th>0×12)</th><th></th><th></th></rena<>	ame@plt+	6>:	push	0×12)		
0176	0x4040b0	>	0×401166	(<spri< th=""><th>intf@plt</th><th>+6>:</th><th>push</th><th>0×13)</th><th></th><th></th></spri<>	intf@plt	+6>:	push	0×13)		
0184	0x4040b8	>	0×401176	(<fwri< th=""><th>ite@plt+</th><th>6>:</th><th>push</th><th>0×14)</th><th></th><th></th></fwri<>	ite@plt+	6>:	push	0×14)		
0192	0x4040c0	>	0×401186	(<slee< th=""><th>ep@plt+6</th><th>i>:</th><th>push</th><th>0×15)</th><th></th><th></th></slee<>	ep@plt+6	i>:	push	0×15)		
0200	0x4040c8	>	0×401196	(<rand< th=""><th>i@plt+6></th><th>•:</th><th>push</th><th>0×16)</th><th></th><th></th></rand<>	i@plt+6>	•:	push	0×16)		
0208	0x4040d0	>	0×0							
0216	0x4040d8	>	0×0							
0224	0x4040e0	>	0×0							
02321	0x4040e8	>	0×0							

Symbol resolution algorithm 3/3

- Stub only called once (the first time)
- Resolver replaces the stub with a direct pointer

gdb-p	<mark>eda\$</mark> teles	cope	e 0x40400	0 30						
0000	0x404000	>	0x403e20	>	0×1					
0008	0×404008	>	0x7ffff7	ffe19	0> 0	×0				
0016	0x404010	>	0x7ffff7	fea44	0 (<_dl	_runtime_	resolve	_xsave>:	push	rbx)
0024	0×404018	>	0x401036	(<fr< th=""><th>ee@plt+</th><th>6>:</th><th>push</th><th>0×0)</th><th></th><th></th></fr<>	ee@plt+	6>:	push	0×0)		
0032	0x404020	>	0×401046	(<un< th=""><th>link@pl</th><th>t+6>:</th><th>push</th><th>0×1)</th><th></th><th></th></un<>	link@pl	t+6>:	push	0×1)		
0040	0x404028	>	0x401056	(<_e	xit@plt	+6>:	push	0×2)		
0048	0x404030	>	0×401066	(<fr< th=""><th>ead@plt</th><th>+6>:</th><th>push</th><th>0x3)</th><th></th><th></th></fr<>	ead@plt	+6>:	push	0x3)		
0056	0x404038	>	0x401076	(<fc< th=""><th>lose@pl</th><th>t+6>:</th><th>push</th><th>0×4)</th><th></th><th></th></fc<>	lose@pl	t+6>:	push	0×4)		
0064	0×404040	>	0x7ffff7	e87f6	0 (<0	pendir>:	cmp	BYTE PTR	[rdi],0	/×0)
0072	0x404048	>	0x7ffff7	f2256	0 (<s< th=""><th>trlen_av</th><th>(2>:</th><th>mov</th><th>ecx,edi)</th><th></th></s<>	trlen_av	(2>:	mov	ecx,edi)	
0080	0×404050	>	0x7ffff7	e87fa	0 (<c< th=""><th>losedir>:</th><th></th><th>test</th><th>rdi,rdi)</th><th></th></c<>	losedir>:		test	rdi,rdi)	
0088	0x404058	>	0x4010b6	(<sr< th=""><th>and@plt</th><th>+6>:</th><th>push</th><th>0x8)</th><th></th><th></th></sr<>	and@plt	+6>:	push	0x8)		
0096	0x404060	>	0x7ffff7	fldaa	0 (<s< th=""><th>trcmp_av></th><th>(2>:</th><th>mov</th><th>eax,edi)</th><th></th></s<>	trcmp_av>	(2>:	mov	eax,edi)	
0104	0×404068	>	0x4010d6	(<ti< th=""><th>me@plt+</th><th>6>:</th><th>push</th><th>0xa)</th><th></th><th></th></ti<>	me@plt+	6>:	push	0xa)		
0112	0x404070	>	0x4010e6	(<	xstat@p	lt+6>:	push	0xb)		
0120	0x404078	>	0x7ffff7	e8816	⊖ (<g< th=""><th>Ireado</th><th>lir64>:</th><th>push</th><th>r13)</th><th></th></g<>	Ireado	lir64>:	push	r13)	
0128	0×404080	>	0x401106	(<fs< th=""><th>eek@plt</th><th>+6>:</th><th>push</th><th>0xd)</th><th></th><th></th></fs<>	eek@plt	+6>:	push	0xd)		
0136	0x404088	>	0×401116	(<pt< th=""><th>race@pl</th><th>t+6>:</th><th>push</th><th>0xe)</th><th></th><th></th></pt<>	race@pl	t+6>:	push	0xe)		
0144	0x404090	>	0x401126	(<as< th=""><th>printf@</th><th>plt+6>:</th><th>push</th><th>0xf)</th><th></th><th></th></as<>	printf@	plt+6>:	push	0xf)		
0152	0×404098	>	0x401136	(<mp< th=""><th>rotect@</th><th>plt+6>:</th><th>push</th><th>0×10)</th><th></th><th></th></mp<>	rotect@	plt+6>:	push	0×10)		
0160	0x4040a0	>	0×401146	(<fo< th=""><th>pen@plt</th><th>+6>:</th><th>push</th><th>0×11)</th><th></th><th></th></fo<>	pen@plt	+6>:	push	0×11)		
0168	0x4040a8	>	0x401156	(<re< th=""><th>name@pl</th><th>t+6>:</th><th>push</th><th>0×12)</th><th></th><th></th></re<>	name@pl	t+6>:	push	0×12)		
0176	0x4040b0	>	0×401166	(<sp< th=""><th>rintf@p</th><th>lt+6>:</th><th>push</th><th>0x13)</th><th></th><th></th></sp<>	rintf@p	lt+6>:	push	0x13)		
0184	0x4040b8	>	0x401176	(<fw< th=""><th>rite@pl</th><th>t+6>:</th><th>push</th><th>0×14)</th><th></th><th></th></fw<>	rite@pl	t+6>:	push	0×14)		
0192	0x4040c0	>	0x401186	(<sl< th=""><th>eep@plt</th><th>+6>:</th><th>push</th><th>0×15)</th><th></th><th></th></sl<>	eep@plt	+6>:	push	0×15)		
0200	0x4040c8	>	0×401196	(<ra< th=""><th>nd@plt+</th><th>6>:</th><th>push</th><th>0×16)</th><th></th><th></th></ra<>	nd@plt+	6>:	push	0×16)		
0208	0x4040d0	>	0x0							
0216	0x4040d8	>	0×0							
0224	0x4040e0	>	0×1							
0232	0x4040e8	>	0×0							

But why do we care?

- The GOT is modifiable by the loader
- The GOT is also a potential target for overwrite
- Exploit a relative arbitrary write from a global buffer
- Exploit an absolute arbitrary write

RELRO mitigation mechanism

- Resolve everything at the start
- Set memory permissions to read only
- Read Only RELocations

gdb-peda\$ telescope 0x403f20 30 00001 0x403f20 --> 0x403d30 --> 0x1 00081 0x403f28 --> 0x0 $0016| 0 \times 403f30 \dots 0 \times 0$ 0024| 0x403f38 --> 0x7ffff7e4abc0 (< _GI _ libc free>: push rbx) 0032| 0x403f40 --> 0x7ffff7eb2290 (<unlink>: eax.0x57) mov 0040 $0 \times 403f48 \longrightarrow 0 \times 7ffff7e8cca0$ (< GI exit>: mov edx.edi) r14) 0048| 0x403f50 --> 0x7ffff7e367f0 (<fread>: push 0056| 0x403f58 --> 0x7ffff7e359e0 (<fclose>: nush r12) 00641 0x403f60 --> 0x7ffff7e87f60 (< opendir>: cmp BYTE PTR [rdi],0x0) 00721 0x403f68 --> 0x7ffff7f22560 (< strlen avx2>: mov ecx.edi) 00801 0x403f70 --> 0x7ffff7e87fa0 (< closedir>: rdi, rdi) test 0088| 0x403f78 --> 0x7ffff7e008f0 (< srandom>: sub rsp.0x8) 00961 0x403f80 --> 0x7ffff7f1daa0 (< strcmp avx2>: mov eax.edi) rax.QWORD PTR [rip+0xfffffffffffffffff1al] 0104 0x403f88 --> 0x7ffff7fd3f00 (<time>: mov 0112| 0x403f90 --> 0x7ffff7eafd60 (< GI xstat>: mov rax, rsi) 0120 0x403f98 --> 0x7ffff7e88160 (< GI readdir64>: push r13) push rbx) 01281 0x403fa0 --> 0x7ffff7e3deb0 (<fseek>: 0136| 0x403fa8 --> 0x7ffff7eb7bf0 (<ptrace>: rsp.0x68) sub 0144| 0x403fb0 --> 0x7ffff7ele950 (< asprintf>: sub rsp,0xd8) 01521 0x403fb8 --> 0x7ffff7eba510 (<mprotect>: mov eax.0xa) 0160| 0x403fc0 --> 0x7ffff7e363e0 (< IO new fopen>: mov edx.0x1) 0168 0x403fc8 --> 0x7ffff7e338d0 (<rename> eax,0x52) mov 0176| 0x403fd0 --> 0x7ffff7e1e890 (< sprintf>: sub rsp.0xd8) 0184 0x403fd8 --> 0x7ffff7e36c10 (<fwrite>: r15) push 0192| 0x403fe0 --> 0x7ffff7e8c910 (< sleep>: nush rbp) 02001 0x403fe8 --> 0x7ffff7e00fc0 (<rand>: sub rsp.0x8) 0208| 0x403ff0 --> 0x7ffff7de9fb0 (< libc start main>: push r14) 0216| 0x403ff8 --> 0x0 02241 0x404000 --> 0x0 02321 0x404008 --> 0x0

RELRO Tradeoff

- The loader needs to do extra work at program startup
- But the loader needs to do less work afterwards
- And the program is more secure without much effort

RELRO Tradeoff

- The loader needs to do extra work at program startup
- But the loader needs to do less work afterwards
- And the program is more secure without much effort
- In practice, GOT tables are still overwritten (but in libraries)

Write What Where

- https://cwe.mitre.org/data/definitions/123.html
- Very Powerful! Overwrite anything
- Using the GOT table:
 - Overwrite free.got with system.got
 - Overwrite puts.got with printf.got
 - Overwrite stack-modifying functions with gets.got
- Endless possibilities

PIE mitigation

- The final 'nail' in the coffin
- The main executable is compiled as a library
- Position Independent Executable
- Kills off many vulnerability classes
- Cost: 20-25% performance penalty

PIE mitigation

- The final 'nail' in the coffin
- The main executable is compiled as a library
- Position Independent Executable
- Kills off many vulnerability classes
- Cost: 20-25% performance penalty
- Believe it or not, it can be bypassed in many situations

Without PIE

gdb-pedaş vmmap		
Start	End	Perm
0×00400000	0×00401000	rp
0×00401000	0×00402000	r-xp
0×00402000	0×00403000	rp
0×00403000	0×00404000	rp
0×00404000	0×00405000	rw-p
0x00007fb7096bc000	0x00007fb7096de000	rp
0x00007fb7096de000	0x00007fb709826000	r-xp
0x00007fb709826000	0x00007fb709872000	rp
0x00007fb709872000	0x00007fb709873000	p
0x00007fb709873000	0x00007fb709877000	rp
0x00007fb709877000	0x00007fb709879000	rw-p
0x00007fb709879000	0x00007fb70987d000	rw-p
0x00007fb70987d000	0x00007fb70987f000	rw-p
0x00007fb7098c6000	0x00007fb7098c7000	rp
0x00007fb7098c7000	0x00007fb7098e5000	r-xp
0x00007fb7098e5000	0x00007fb7098ed000	rp
0x00007fb7098ed000	0x00007fb7098ee000	rp
0x00007fb7098ee000	0x00007fb7098ef000	rw-p
0x00007fb7098ef000	0x00007fb7098f0000	rw-p
0x00007fffb2512000	0x00007fffb2533000	rw-p
0x00007fffb2594000	0x00007fffb2597000	rp
0x00007fffb2597000	0x00007fffb2599000	r-xp
adb-peda\$		

Name

/ctf/unibuc/curs re/curs 07/demo04 pie/asg1 /lib/x86 64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so mapped mapped /lib/x86 64-linux-gnu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so /lib/x86_64-linux-anu/ld-2.28.so mapped [stack] [vvar] [vdso]

With PIE

gdb-peda\$ vmmap

Start End Perm 0x0000561973f33000 0x0000561973f34000 r--n 0x0000561973f34000 0x0000561973f35000 r-xp 0x0000561973f35000 0x0000561973f36000 r--p 0x0000561973f36000 0x0000561973f37000 r--p 0x0000561973f37000 0x0000561973f38000 rw-p 0x00007f561835c000 0x00007f561837e000 r--p 0x00007f561837e000 0x00007f56184c6000 r-xp 0x00007f56184c6000 0x00007f5618512000 r--p 0x00007f5618512000 0x00007f5618513000 ---p 0x00007f5618513000 0x00007f5618517000 r--p 0x00007f5618517000 0x00007f5618519000 rw-p 0x00007f5618519000 0x00007f561851d000 rw-p 0x00007f561851d000 0x00007f561851f000 rw-p 0x00007f5618566000 0x00007f5618567000 r--p 0x00007f5618567000 0x00007f5618585000 r-xp 0x00007f5618585000 0x00007f561858d000 r--p 0x00007f561858d000 0x00007f561858e000 r--p 0x00007f561858e000 0x00007f561858f000 rw-p 0x00007f561858f000 0x00007f5618590000 rw-p 0x00007ffef0e71000 0x00007ffef0e92000 rw-p 0x00007ffef0f8d000 0x00007ffef0f90000 r--p 0x00007ffef0f90000 0x00007ffef0f92000 r-xp ddb-peda\$

Name

/ctf/unibuc/curs re/curs 07/demo04 pie/asg1 /ctf/unibuc/curs re/curs 07/demo04 pie/asg1 /ctf/unibuc/curs_re/curs_07/demo04_pie/asol /ctf/unibuc/curs re/curs 07/demo04 pie/asg1 /ctf/unibuc/curs re/curs 07/demo04 pie/asg1 /lib/x86 64-linux-gnu/libc-2.28.so /lib/x86_64-linux-anu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so /lib/x86_64-linux-gnu/libc-2.28.so mapped manned /lib/x86 64-linux-gnu/ld-2.28.so /lib/x86_64-linux-anu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so /lib/x86_64-linux-gnu/ld-2.28.so mapped [stack] [vvar] [vdso]

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Compiler defaults

■ PIE is on by default (Linux, Windows)

Compiler defaults

- PIE is on by default (Linux, Windows)
- RELRO is off by default on Linux
- RELRO is on by default on Windows

Compiler defaults

- PIE is on by default (Linux, Windows)
- RELRO is off by default on Linux
- RELRO is on by default on Windows
- SSP is off by default on Linux
- SSP (as GS) is on by default on Windows

Final remarks

- Some ASLR bypass is still needed (and usually found)
- All libraries/dependencies become the new attack surface
- Only works if you have the EXACT binaries at hand
- GOT tables are just a particular case of function pointers
- There are no libraries without read/write function pointers

Practice

- Any Questions?
- http://pwnthybytes.ro/unibuc_re/07-lab.html